CONGRATULATES

SRC/SIA System-on-Chip (SoC) Design Challenge Second Place Team

Electrical engineering graduate students Adam Cabe, Wei Huang, Garrett Rose, Zhenyu “Jerry” Qi, Wenqian “Wendy” Wu and Yan Zhang, led by Associate Professor Mircea R. Stan of the Charles L. Brown Department of Electrical and Computer Engineering

HONORED BY

The Semiconductor Research Corporation and the Semiconductor Industry Association

For their entry, “An SRAD Image Processor as a Reconfigurable, Temperature-Aware SoC Designed for Low-Power Operation”
Electrical Engineering Graduate Students Take Second Place in 2004-2006 SRC/SIA System-on-Chip Design Challenge

Associate Professor Mircea Stan led a team of six University of Virginia electrical engineering graduate students to a second place finish in the final phase of the SRC/SIA System-on-Chip (SoC) Design Challenge. The competition featured entries from 39 of the best university semiconductor engineering programs in the country and concluded in October of 2006. The team’s design entry, “An SRAD Image Processor as a Reconfigurable, Temperature-Aware SoC Designed for Low-Power Operation,” was a low-power SoC for portable image processing applications that uses an efficient algorithm to increase signal-to-noise performance. The design could be used to reduce noise or speckling in the images produced by portable ultrasound machines.

The SoC Design Challenge exists to encourage university faculty and their graduate students to create novel, low power, robust SoC designs that demonstrate the value of greater systems integration and to promote education in integrated circuit design.

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